	Search Text
1	bus adj width and (pld programmable adj logic adj device)
2	bus adj width and (pld programmable adj logic adj device)
3	bus adj width and (pld programmable adj logic adj device) and (bus adj based adj design)
4	bus adj width and (pld programmable adj logic adj device) and (bus near5 (bit word))
5	bus adj width and (pld programmable adj logic adj device) and (bus near5 (bit word)) and @ad<=20030711
6	bus adj width and (pld programmable adj logic adj device) and (bus near5 (bit word)) and @ad<=20030711 and function$4 near3 represent$5 and system near3 component
7	bus adj width and (pld programmable adj logic adj device) and (bus adj protocol) and @ad<=20030711 and function$4 near3 represent$5 and system near3 component
8	bus near4 (width wide word) and (pld programmable adj logic adj device) and (bus adj protocol) and @ad<=20030711 and function$4 near3 represent$5 and system near3 component
9	bus adj width and (pld programmable adj logic adj device) and (bus near5 (bit wide word)) and @ad<=20030711 .
10	bus adj width and (pld programmable adj logic adj device fpga) and (bus near5 (bit wide word)) and @ad<=20030711 .
11	bus near4 width and (pld programmable adj logic adj device fpga) and (bus near5 (bit wide word)) and @ad<=20030711 .
12	bus near4 width and (pld programmable adj logic adj device fpga) and (bus near5 (bit wide word protocol)) and @ad<=20030711 .
13	bus$3 near4 width and (pld programmable adj logic adj device fpga) and (bus$4 near5 (bit wide word protocol)) and @ad<=20030711 .
14	bus$3 near4 width
15	bus$3 near4 width and bus near4 deisgn$4 and bus near4 protocol
16	bus$3 near4 width and bus near4 design$4 and bus near4 protocol
17	bus$3 near4 width and bus near4 design$4 and bus near4 protocol and (pld programmable adj logic adj device fpga)
18	bus$3 near4 width and bus near4 design$4 and bus near4 protocol and (pld programmable adj logic adj device fpga) near3 (design$4 config$7 fig$5)
19	bus$3 near4 width and bus near4 (protocol design$4) and (pld programmable adj logic adj device fpga) near3 (design$4 config$7 fig$5) and bus$3 near5 (compati$7 common)
20	bus$3 near4 width and bus near4 (protocol design$4) and (pld programmable adj logic adj device fpga) near3 (design$4 config$7 fig$5) and bus$3 with (compati$7 common) with component
21	bus$3 near4 width and bus near4 (protocol design$4) and (pld programmable adj logic adj device fpga) near3 (design$4 config$7 fig$5) and bus$3 near6 (compati$7 common) and port near3 name
22	bus$3 with (width (number near3 bit) wide) and bus near4 (communication protocol design$4) and (pld programmable adj logic adj device fpga) near3 (design$4 config$7 fig$5) and bus$3 near6 (allow$4 compati$7 common) and port near3 name
23	bus$3 near4 (width bit wide word) and bus near4 (protocol design$4) and (pld programmable near4 (logic field) adj device fpga) and bus$3 with (compati$7 common) with component
24	bus$3 near4 (width bit wide word) and bus near4 (protocol design$4) and (pld programmable near4 (logic field) adj device fpga) near3 (design$4 config$7 fig$5) and bus$3 with (compati$7 common) with component
25	bus$3 near4 (width bit wide word) and bus near4 (protocol design$4) and (pld programmable near4 (logic field) adj device fpga) near3 (design$4 config$7 fig$5) and bus$3 with (compati$7 common) with component and @ad<=20030711

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26	(bus$3 near4 (width bit wide word) and bus near4 (protocol design$4) and (pld programmable near4 (logic field) adj device fpga) near3 (design$4 config$7 fig$5) and bus$3 with (compati$7 common) with component and @ad<=20030711) and bus$4 near5 width
27	bus$3 near4 (width bit wide word) and bus near4 (protocol design$4) and (pld programmable near4 (logic field) adj device fpga) and bus$3 with (compati$7 common) and @ad<=20030711 and 716/$.ccls.
28	(bus$3 near4 (width bit wide word) and bus near4 (protocol design$4) and (pld programmable near4 (logic field) adj device fpga) and bus$3 with (compati$7 common) and @ad<=20030711 and 716/$.ccls.) and bus$4 same width
29	(bus$3 near4 (width bit wide word) and bus near4 (protocol design$4) and (pld programmable near4 (logic field) adj device fpga) and bus$3 with (compati$7 common) and @ad<=20030711 and 716/$.ccls.) and bus$4 same width same differ$6
30	(bus$3 near4 (width bit wide word) and bus near4 (protocol design$4) and (pld programmable near4 (logic field) adj device fpga) and bus$3 with (compati$7 common) and @ad<=20030711 and 716/$.ccls.) and bus$4 same width same (common compa$7)
31	bus$3 near4 (width bit wide word) and bus near4 (protocol design$4) and (pld programmable near4 (logic field) adj device fpga) near3 (design$4 config$7 fig$5) and bus$3 with (compati$7 common) with component and @ad<=20030711 and depend$7 with bus$4
32	bus$4 same width same (compa$7) same (line connection)
33	bus$4 same width same (compa$7) same (line connection) and (pld programmable near4 (logic field) adj device fpga) and @ad<=20030711 and 716/$.ccls.
34	bus$4 same width same (compa$7) and (pld fpga programmable near4 (logic field device)) and @ad<=20030711 and 716/$.ccls.
35	bus$4 same (width word) same (compa$7) and (pld fpga programmable near4 (logic field device)) and @ad<=20030711 and 716/$.ccls. and paul near3 dinh